NO.479

D001

MAR 1 5 2007



**LSI Logic Corporation** Legal Department - IP 1621 Barber Lane, MS D-106 Milpitas, CA 95035

Date March 15, 2007	
Number of pages including cover sheet	<u>2</u>
To: US PTO	From: Connie del Castillo
Mail Stop Status Request	Intellectual Property Paralegal
TC 2181	Telephone (408) 433-7191 No.
Fax No. <b>571-273-8300</b> Phone No.	Fax No. (408) 433-7460
CC:	
REMARKS:	For your review ✓ Reply ASAP ☐ Please comment
Application Number: 10/718,286 Filing date: November 1 First named inventor: Bagriel Rom Attorney docket number: 03-0760 / L	nero_
Transmitted herewith for filing via facsing  • Status Request Letter	ile:
Pursuant to 37 C.F.R. 1.8, I hereby certify Patent and Trademark Office on the date	that this correspondence is being transmitted by facsimile to the U.S. indicated below:
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09:38

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NO.479 D002

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Gabriel Romero

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MAR 1 5 2007

Serial No.:

10/718,286

Group Art Unit:

Filed:

11/19/2003

Examiner:

Jesse R. Moll

2181

For:

Methodology For Performing

Register Read/Writes To Two Or

Atty Docket:

LSI.87US01 / 03-0760

More Expanders With A Common Test Port

## STATUS REPORT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milpitas, CA 95035 408-433-7475

Date: 3/15 107

Respectfully submitted,

Peter Scott, Ph: [+1] 408-433-7191

Reg. No. 33,279